



TITLE: A METHOD AND SYSTEM FOR DYNAMIC POWER SUPPLY VOLTAGE
ADJUSTMENT FOR A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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100

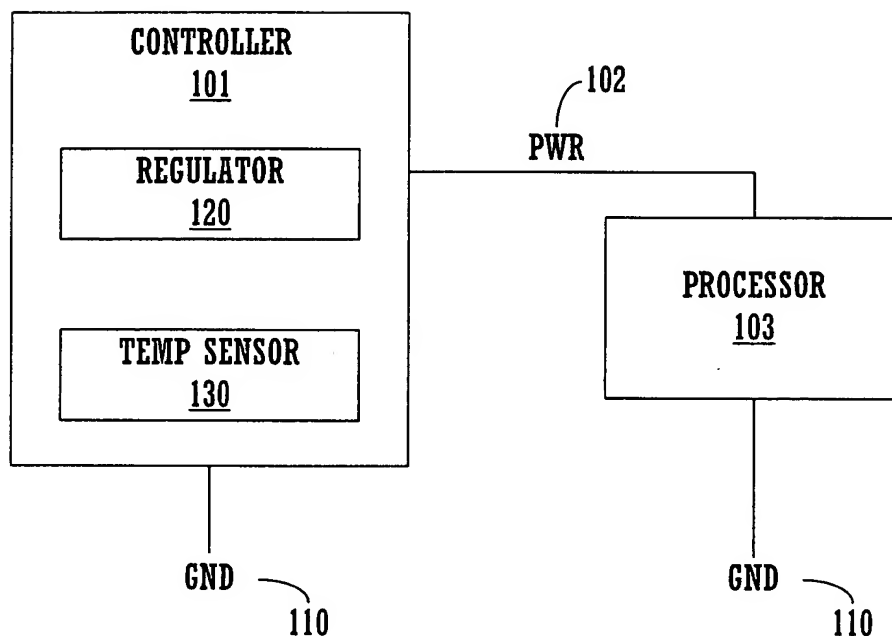


FIGURE 1

200

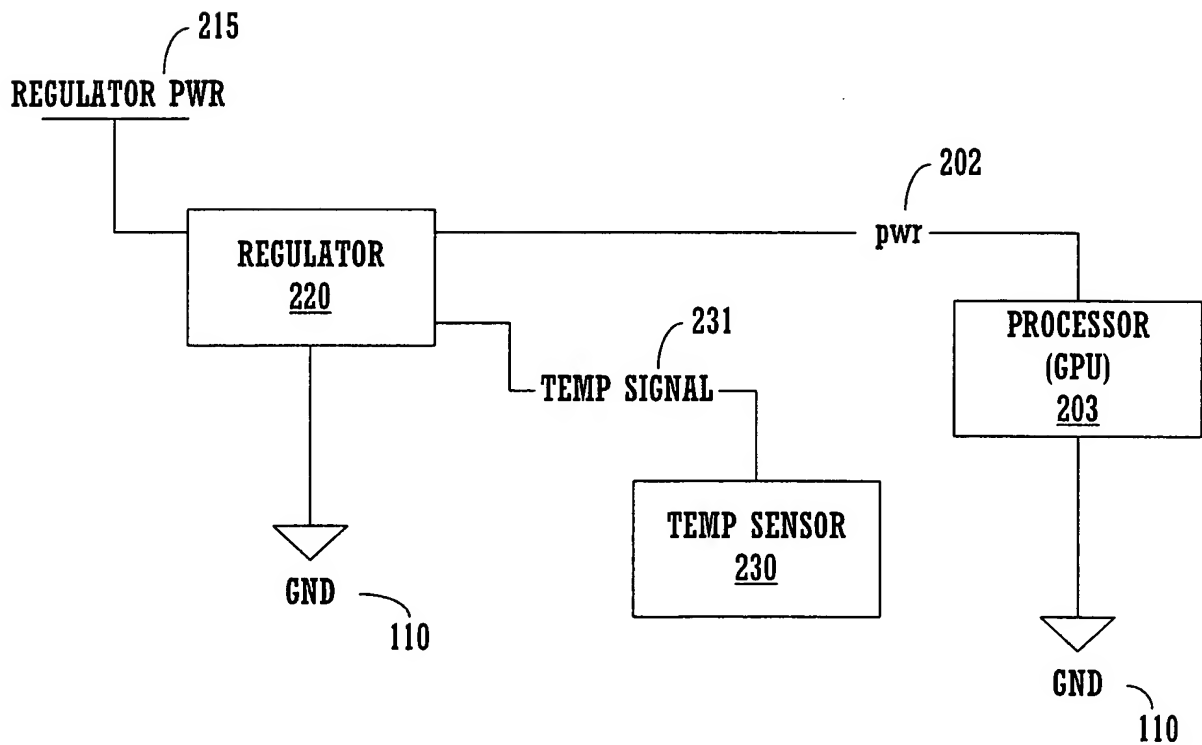


FIGURE 2

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300

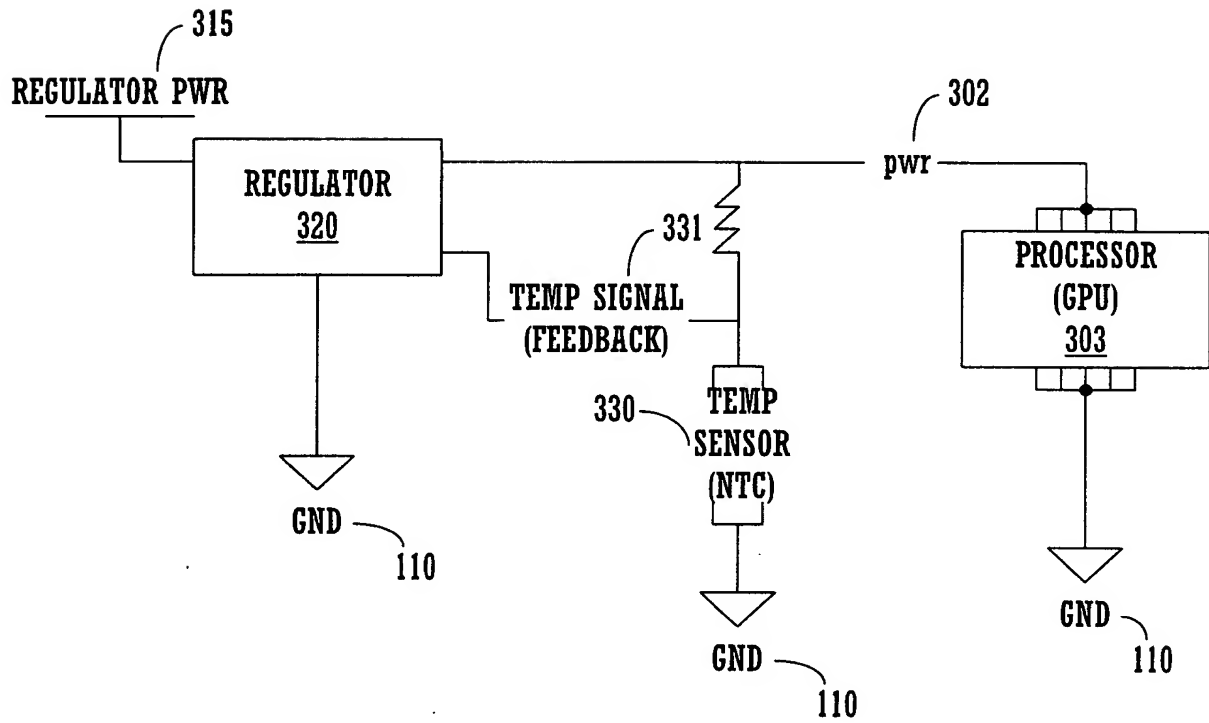


FIGURE 3

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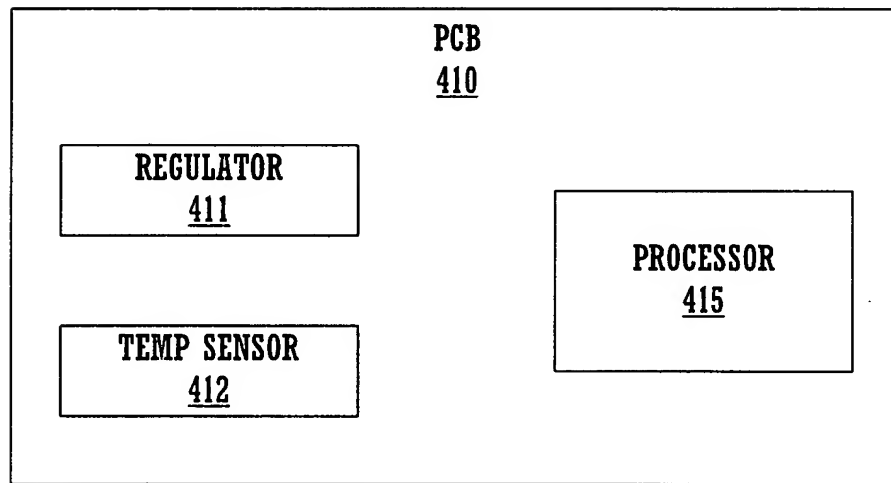


FIGURE 4A

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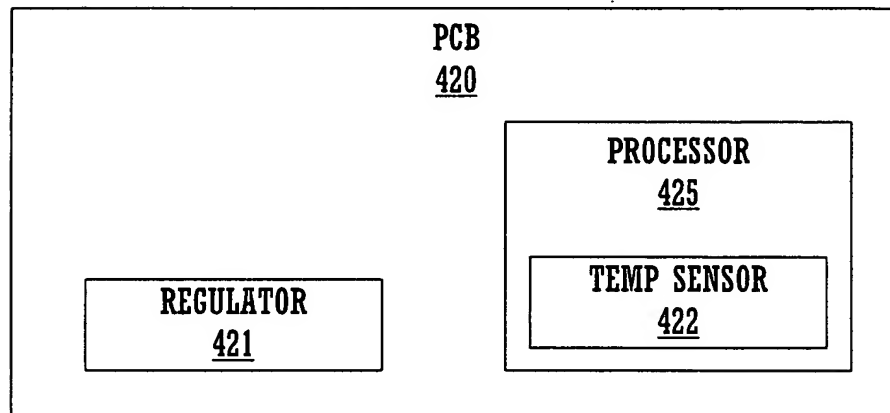


FIGURE 4B

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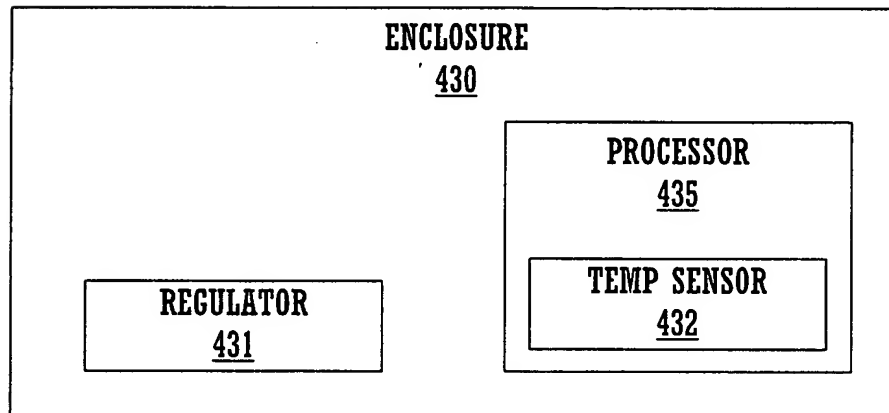


FIGURE 4C

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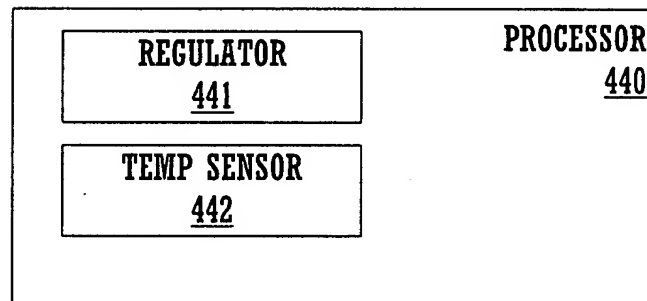


FIGURE 4D

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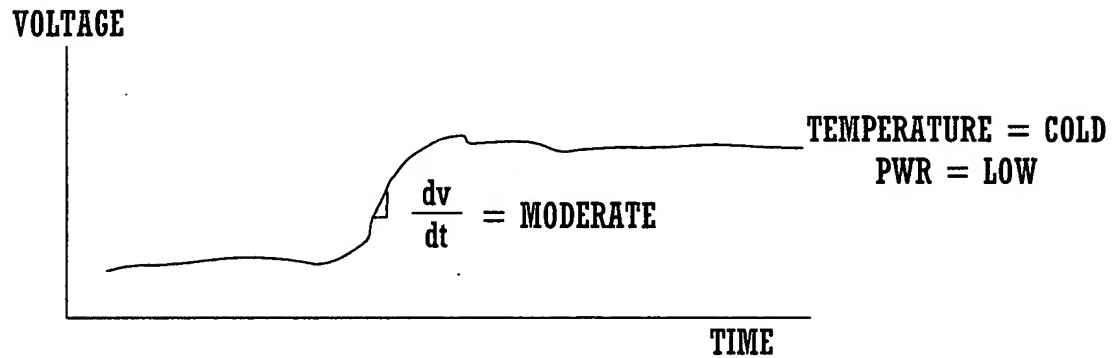


FIGURE 5A

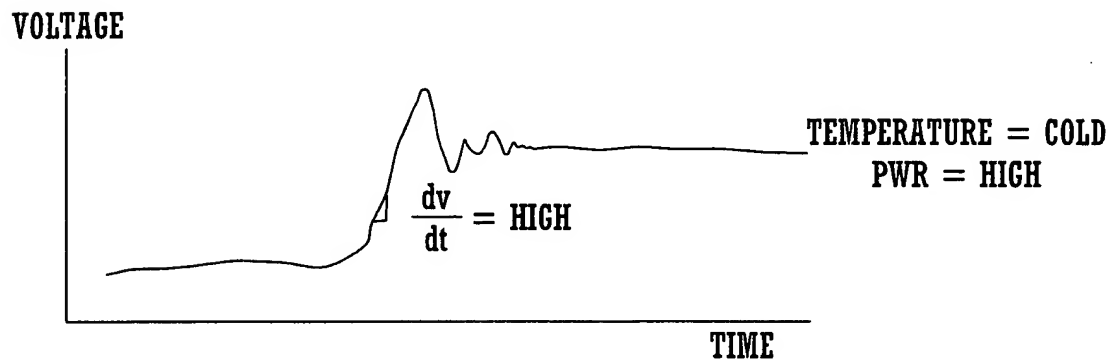


FIGURE 5B

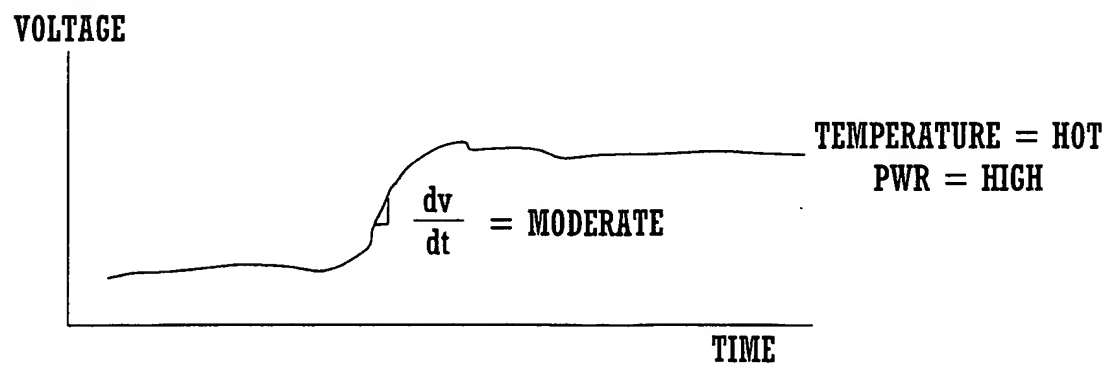


FIGURE 5C

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600

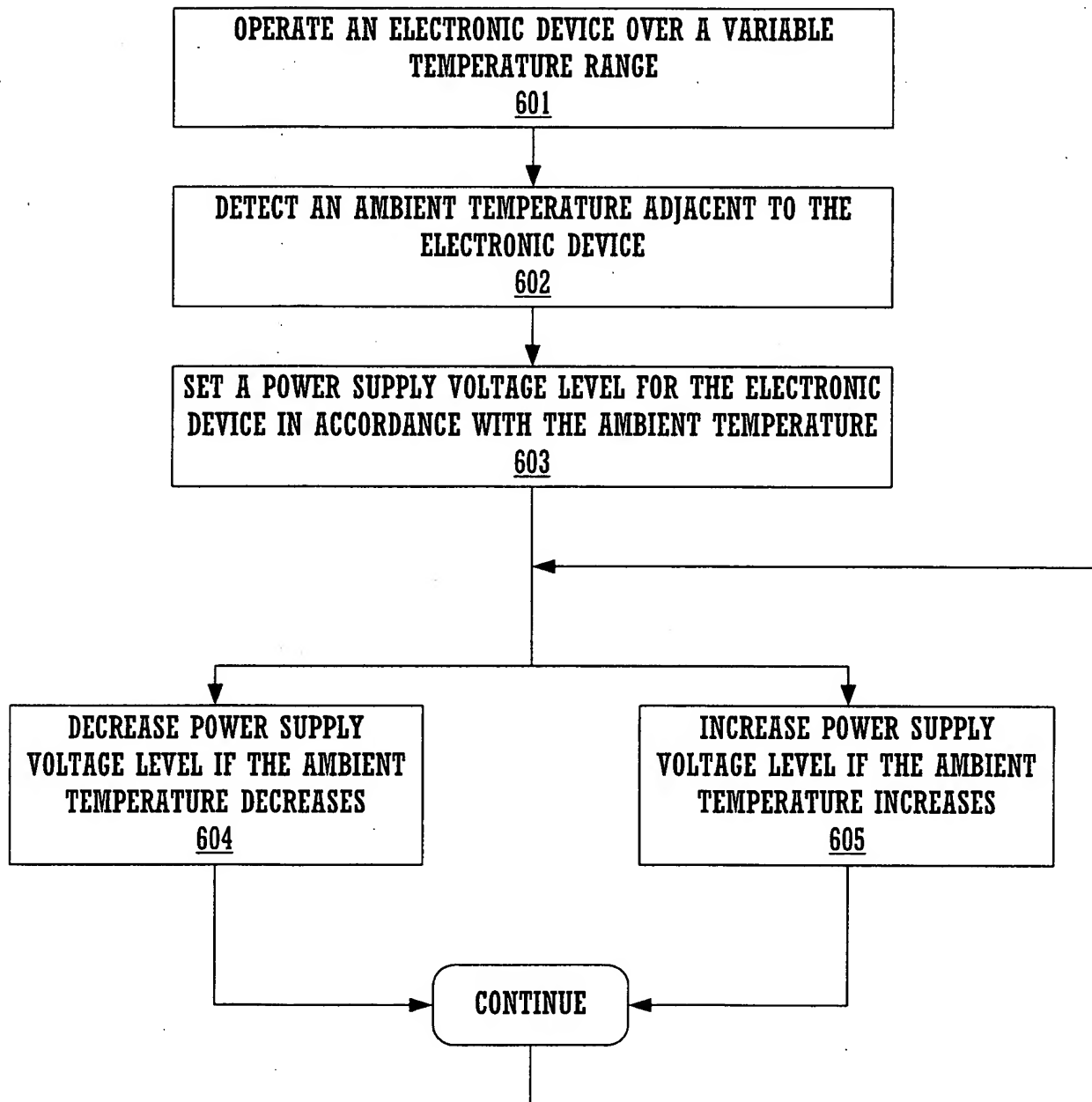


FIGURE 6